

Digital Computer Laboratory
 Massachusetts Institute of Technology
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SUBJECT: INTERNAL DOCUMENTS ON FERROMAGNETIC AND FERROELECTRIC CORES

To: N. H. Taylor

From: W. N. Papian

Date: January 18, 1952, Revised June 10, 1952

Abstract: A listing of Reports, Engineering Notes, and Memoranda on various aspects of the ferromagnetic core activity is presented.

	<u>Title</u>	<u>Date</u>	<u>Author</u>
<u>Reports</u>			
R-187	Digital Information Storage in Three Dimensions Using Magnetic Cores	5-16-50	J. W. Forrester
R-192	A Coincident-Current Magnetic Memory Unit (S.M. Thesis. Abstract in L-379)	9-8-50	W. N. Papian
R-211	A Magnetic Matrix Switch and Its Incorporation into A Coincident-Current Memory (S.M. Thesis)	6-6-52	A. H. Olsen
R-212	Ferroelectric for Digital Information Storage and Switching (S.M. Thesis)	6-5-52	D. A. Buck

Summary Reports

SR-24	Third Quarter 1950 pp 11-14
SR-25	Fourth Quarter 1950 & First Quarter 1951 p.16
SR-26	Second Quarter 1951 pp 12-13
SR-27	Third Quarter 1951 pp 10-14
SR-28	Fourth Quarter 1951 pp 11-13


	<u>Title.</u>	<u>Date</u>	<u>Author</u>
<u>Plans (Not in Report Form)</u>			
1812	A Magnetic Flip-Flop	5-16-52	R. J. Pfaff

Engineering Notes

E-416	Preliminary Tests on the Semi-Core Magnetic-Memory Array	6-18-51	G. N. Fapian
E-418	Selection System for Magnetic Core Storage	8-7-51	H. R. Everett
E-422	Rectangular-Loop Magnetic Core Materials	9-2-51	G. N. Fapian
E-438	Binary Counting with Magnetic Cores	12-6-51	D. A. Buck
E-454	A Non-Destructive Read System For Magnetic Cores	3-24-52	D. A. Buck
E-460	The Ferroelectric Switch	4-16-52	D. A. Buck

	<u>Title</u>	<u>Date</u>	<u>Author</u>
<u>Memoranda</u>			
M-975	Master's Thesis Proposal: A Coincident-Current Magnetic Memory Unit	1-24-50	W. N. Papian
M-1282	S.M. Thesis Proposal: A Multi-Position Magnetic Switch and Its Incorporation into a Magnetic Memory	9-21-51	K. L. Olsen
M-1335	S.M. Thesis Proposal: An Investigation of Ferroelectrics for Digital Information Storage	11-23-51	L. A. Buck
M-1369	Trip to General Ceramics January 9, 1952	1-11-52	L. R. Brown
M-1371	Magnetic Core Activity	1-15-52	W. N. Papian
M-1381	Magnetic-Core Memory Matrix Analysis (Effect of Driver Impedence)	1-24-52	D. A. Buck
M-1429	A Binary Adder Using Magnetic-Core Flip-Flop	3-19-52	R. G. Sims
M-1477	Design of Low-Power Pulse Transformers Using Ferrite Cores	5-5-52	R. D. Robinson
M-1490	Procedure for Receiving Magnetic Cores	5-16-52	D. R. Brown

SIGNED 
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APPROVED 
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