

Memorandum M-1669

Page 1 of 5

Digital Computer Laboratory  
Massachusetts Institute of Technology  
Cambridge 39, Massachusetts

SUBJECT: INTERNAL PUBLICATIONS OF THE ARITHMETIC ELEMENT SECTION

To: Norman H. Taylor

From: John F. Jacobs

Date: October 7, 1952

This memo supersedes M-1465, "Internal Reports of the Transistor Group".

M-Notes

<u>Analysis</u>	<u>Author</u>	<u>Title</u>
M-1400	J.F. Jacobs	STABILIZED TRANSISTOR AS A FOUR-TERMINAL NON-LINEAR NETWORK*
M-1406	J.F. Jacobs, N.T. Jones	GENERAL NOTES ON NEGATIVE RESISTANCE TRANSISTOR CIRCUITS*
M-1430	A.W. Heineck W.A. Klein	EMITTER AND BASE TRIGGERING OF A SINGLE TRANSISTOR, BASE STABILIZED FLIP-FLOP
M-1442	J.F. Jacobs W.A. Klein	LINEARIZED CHARACTERISTICS OF A BASE FED, GROUNDED EMITTER TRANSISTOR*
M-1662	W.A. Klein	ANALYSIS OF THE TRANSISTOR EQUIVALENT CIRCUIT

Computer  
Circuits

M-1433	A.W. Heineck	A POSITIVE OR NEGATIVE REGENERATIVE TRANSISTOR PULSE AMPLIFIER
M-1459	D. Eckl	PRELIMINARY DESIGN OF TRANSISTOR TEST ACCUMULATOR AND A-REGISTER

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\* Results of these Notes are summarized in M-1662

M-Notes (cont.)

<u>Computer Circuits</u>	<u>Author</u>	<u>Title</u>
M-1478	J.F. Jacobs W.A. Klein	TRANSISTOR CIRCUITS AS REALIZATIONS OF BOOLEAN FUNCTIONS
M-1480	R.J. Callahan	A TRANSISTOR "AND" GATE***
M-1555	A.W. Heineck	A THREE-MEGACYCLE TRANSISTOR FLIP-FLOP

Thesis  
Proposals \*\*

M-1353	J.F. Jacobs	A HIGH-SPEED COUNTER EMPLOYING TRANSISTORS***
M-1401	A.W. Heineck	REGENERATIVE TRANSISTOR PULSE AMPLIFIERS
M-1519	A.M. Heien	A HIGH-SPEED FLIP-FLOP CIRCUIT USING JUNCTION TRANSISTORS

Transistor  
Measurements &  
Specifications

M-1404	N.T. Jones	TRANSISTOR GROUP EXPERIMENTAL PROCEDURES
M-1541	N.T. Jones	BRIEF DESCRIPTION OF TRANSISTOR TYPES
M-1540-1	J.F. Jacobs	NOTES ON THE SPECIFICATIONS OF THE RCA TA-165
M-1610	N. T. Jones	EVALUATION OF SAMPLE FRENCH WESTCREL TRAN- SISTORS
M-1620	N.T. Jones	INTERPRETATION OF TRANSISTOR DATA CARDS

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\*\* See Thesis and R Notes for results

\*\*\* Results are reported in R-214

M-Notes (cont.)

<u>Trips</u>	<u>Author</u>	<u>Title</u>
M-1300	D.R. Brown	TRIP TO BELL LABORATORIES, WHIPPANY
M-1607	J. Woolf	RAYTHEON TOUR CONDUCTED BY GEORGE FREEDMAN, HERB STARK

E-Notes

Analysis

E-435	D.R. Brown, J.F. Jacobs, N.T. Jones	STUDY OF A TRANSISTOR BLOCKING OSCILLATOR
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Bibliography

E-421-1	N.T. Jones	TRANSISTOR BIBLIOGRAPHY
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Computer  
Circuits

E-461	J.F. Jacobs, R.J. Callahan	A HIGH-SPEED TWO-TRANSISTOR FLIP-FLOP****
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Transistor  
Measurements &  
Specifications

E-441-1	N.T. Jones	STANDARDIZED TRANSISTOR PARAMETER MEASURE- MENTS
E-447	J.F. Jacobs, N.T. Jones	TRANSISTOR PARAMETER VARIATIONS
E-448	N.T. Jones, J.F. Jacobs	VARIATION OF TRANSISTOR COLLECTOR RESISTANCE DUE TO SELF-HEATING

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\*\*\*\* Results are reported in R-214

E-Notes (cont.)

Transistor  
Measurements &  
Specifications

	<u>Author</u>	<u>Title</u>
E-451	N.T. Jones, R.J. Callahan	VARIATION OF TRANSISTOR COLLECTOR RESISTANCE WITH COLLECTOR VOLTAGE
E-455	N.T. Jones	MEASUREMENT OF COLLECTOR CURRENT RISE AND FALL TIMES IN TRANSISTORS
E-480	J.F. Jacobs	EXPERIENCE WITH GENERAL ELECTRIC G11A TRANSISTORS
E-483	R. Schmidt	RESULTS OF TRANSISTOR THERMAL EXPERIMENTS
E-485	I. Aronson	TRANSISTOR TEST EQUIPMENT

Semi-Conductor  
Physics

E-463	D. Eckl	INTRODUCTION TO THE THEORY OF SEMI- CONDUCTORS I, SOME REMARKS ON QUANTUM MECHANICS
E-468	D. Eckl	INTRODUCTION TO THE THEORY OF SEMI- CONDUCTORS II, A BRIEF DISCUSSION OF STATISTICAL MECHANICS
E-474	D. Eckl	INTRODUCTION TO THE THEORY OF SEMI- CONDUCTORS III, CONDUCTION IN METALS; THE FIELD-FREE CASE

Theses & R-Notes

R-214	J.F. Jacobs	A HIGH-SPEED COUNTER EMPLOYING TRANSISTORS, S.M. Thesis
Thesis	A.W. Heineck	REGENERATIVE TRANSISTOR PULSE AMPLIFIERS, S.M. Thesis
Thesis	A.M. Heien	A HIGH-SPEED FLIP-FLOP CIRCUIT USING JUNCTION TRANSISTORS, S.M. Thesis

Memorandum M-1669

Page 5 of 5

E-Notes (cont.)

<u>Logic of Arithmetic Elements</u>	<u>Author</u>	<u>Title</u>
E-478	J.F. Jacobs, R.L. Best	SEAC LOGIC AND CIRCUITS

Signed J F Jacobs  
John F. Jacobs

Approved NHT  
Norman H. Taylor

JFJ/cs