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Memorandum M-1465

Digital Computer Laboratory
Massachusetts Institute of Technology
Cambridge, Massachusetts

SUBJECT: INTERNAL REPORTS OF THE TRANSISTOR GROUP

To: Norman Taylor
From: J. F. Jacobs
Date: April 24, 1952

This memo is a summary of the internal reports (M and E notes) which have been written by the Transistor Group.

Note	Author	Title
M-1353	John Jacobs	A HIGH-SPEED COUNTER EMPLOYING TRANSISTORS
M-1400	John Jacobs	STABILIZED TRANSISTOR AS A FOUR TERMINAL NON-LINEAR NETWORK
M-1401	A. W. Heineck	REGENERATIVE TRANSISTOR PULSE AMPLIFIERS
M-1404	Nolan T. Jones	TRANSISTOR GROUP EXPERIMENTAL PROCEDURES
M-1406	J. F. Jacobs	GENERAL NOTES ON NEGATIVE RESISTANCE
	N. T. Jones	TRANSISTOR CIRCUITS
M-1430	Arthur Heineck	EMITTER AND BASE TRIGGERING OF A SINGLE TRANSISTOR, BASE-STABILIZED FLIP-FLOP
M-1433	W. A. Klein	A POSITIVE OR NEGATIVE REGENERATIVE TRANSISTOR PULSE AMPLIFIER
M-1442	J. F. Jacobs	LINEARIZED CHARACTERISTICS OF A BASE FED GROUNDED EMITTER TRANSISTOR
M-1459	W. A. Klein	PRELIMINARY DESIGN OF TRANSISTOR TEST ACCUMULATOR AND A-REGISTER
E-421-1	D. Eckl	TRANSISTOR BIBLIOGRAPHY
E-435	N. T. Jones	STUDY OF A TRANSISTOR BLOCKING OSCILLATOR
	D. R. Brown	
	J. F. Jacobs	
	N. T. Jones	
E-441	J. F. Jacobs	STANDARDIZED TRANSISTOR PARAMETER MEASUREMENTS
	N. T. Jones	
E-447	J. F. Jacobs	TRANSISTOR PARAMETER VARIATIONS
	N. T. Jones	
E-448	N. T. Jones	VARIATION OF TRANSISTOR COLLECTOR RESISTANCE DUE TO SELF HEATING
	J. F. Jacobs	
E-451	N. T. Jones	VARIATION OF TRANSISTOR COLLECTOR RESISTANCE WITH COLLECTOR VOLTAGE
	R. J. Callahan	
E-455	N. T. Jones	MEASUREMENT OF COLLECTOR CURRENT RISE AND FALL TIMES IN TRANSISTORS
E-461	J. F. Jacobs	A HIGH-SPEED TWO-TRANSISTOR FLIP-FLOP

Signed

John F. Jacobs
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