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Memorandum M-200

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Project Whirlwind  
Servomechanisms Laboratory  
Massachusetts Institute of Technology  
Cambridge, Massachusetts

SUBJECT: CIRCUITS FOR MARGINAL CHECKING

To: H. Fahnestock, N. Taylor, D. Brown

From: Jay W. Forrester

Date: December 31, 1947

I understand that in making up circuit schematics we are bringing out separately from each different tube application the plate circuit, screen circuit, control grid bias and suppressor grid bias. On flip flops these circuits will be brought out separately for the two sides of the flip-flop, corresponding sides of flip flops of one panel being tied together.

Where bias and clamping circuits are returned to ground, provision will be made for bringing out a lead which can be used for increasing and decreasing the bias voltage above and below ground. This should be handled as a proposal and included in Whirlwind I Specifications.



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Jay W. Forrester

JWF:bc