SUBJECT: GROUP LEADERS' MEETING, JULY 21, 1952

To: Group Leaders
From: David R. Brown
Date: July 21, 1952


Agenda: 1. Electronics and Machine Shops
2. Memory Test Computer
3. Transistor Accumulator

1. Electronics and Machine Shops

Brown discussed the progress of several panels through our electronics shop. In the first case, two panels were involved for magnetic-core tester Model II, construction requisition nos. 1607 and 1040. Sketches for this job were delivered to the Drafting Room on approximately April 1, 1952. The drawings were finished in June; the construction requisition is dated June 18. The first pair of panels was delivered on July 17. In order to shorten the time in the shop, painting and engraving were bypassed. Poor electronic assembly required considerable reconstruction in the shop. Even after the panel had been through our inspection department twice, some errors such as the use of 10 per cent resistors in place of 5 per cent resistors were discovered. In another case of a magnetic-core tester model IV, construction requisition no. 1704, the requisition was dated May 20, 1952, and the panels were promised on June 25. They were delivered on July 11, 1952. In this second case, however, construction was made from sketches rather than formal drawings.

This situation is largely due to the fact that experienced technicians cannot be obtained and we are training our own technicians for the electronics shop. The situation will improve with time and our inspection procedure is an important factor in improving the electronic shop's practice. Scheduling may be improved if more production jobs can be sent outside to subcontractors. Both the electronics shop and the machine shop, however, should be set up so that small jobs can be rushed through with a minimum of delay. The direct assignment of technicians to engineers is also urged as a method of getting small jobs done on a rush basis.
2. Memory Test Computer

The new name for NMIA is NTC (Memory Test Computer). Taylor read a brief discussion of the NTC.

3. Transistor Accumulator

The term "Transistor Accumulator" will be used to describe the four-digit breadboard transistor accumulator now being tested in the Arithmetic Element Section. The term Transac I will not be used in print.

Signed, [Signature]
David R. Brown
Secretary

DRA/JS