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Memorandum 6M-3471

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Division 6 - Lincoln Laboratory
Massachusetts Institute of Technology
Lexington 73, Massachusetts

SUBJECT: SAGE System Meeting March 21, 1955

To: See Distribution List

From: P. Bragar

Date: 22 March 1955

Approved:


A. P. Kromer

- Agenda:
1. Large Board Display
 2. Report on Computer Programming Meeting
 3. Meeting on XD-1 Usage
 4. Data Links
 5. Ferrite Memory Core Specifications
 6. XD-1 Telephone System Money

1. Large Board Display

Slides taken from a 5" tube using P11 phosphor were demonstrated. Work is continuing on possible adaptation of a 5" tube to a situation display console for projection photography. Work is also being done to overcome the difficult optical problems presented by the curved face of the 19" tube.

2. Report of Computer Programming Meeting

The various problems concerned with formulation, adaptation, and revision of the SAGE System computer program were discussed at a meeting held in New York on 17 March and attended by all agencies concerned. All organizations are to report in writing to the JPO by 31 March on the number of people, time phasing, and budgeting of each organization's particular phase of the programming effort. A meeting will be held on 6 April to resolve any remaining problems in this area.

3. Meeting on XD-1 Usage

Taylor and Israel will attend a meeting at AFCRC on 24 March at which a review of the potential users of XD-1 in areas concerned with weapons will be discussed.

4. Data Links


The GE Data Link is to be ordered for the experimental subsector. The type data link to be used in the SAGE System has not yet been decided. Lincoln and IBM are working jointly on a study of FSQ-7 output system compatibility with the different data links. Rising has prepared a proposal for using either the BTL data link or a revised GE data link, both of which would be time division systems (GE is currently a frequency division link).

5. Ferrite Memory Core Specifications

Brown and Carson will attend a meeting in Poughkeepsie on 23 March to discuss the status of ferrite memory core specifications. The critical function of the cores in the system indicates a specification on which Lincoln and IBM can concur with subsequent formal release by Lincoln.

6. XD-1 Telephone System Money

AFCRC has asked for an estimate by 28 March of the cost of the XD-1 Telephone System. Current estimate on the leased circuit cost is about \$500,000 a year. The final figures for the internal system must await receipt by New England Telephone & Telegraph Co. of SAGE System estimate figures from American Telephone & Telegraph Co. However, a good estimate of this cost is in the range of 10% to 15% of the leased circuit cost.

SIGNED: 
P. Bragar

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Meeting Attendants

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H. J. Kirshner
H. E. Anderson

D. R. Israel
D. R. Brown
K. E. McVicar
J. C. Proctor
B. B. Paine

Others

P. Youtz
W. S. Attridge, Jr.
C. A. Zraket
P. Bagley
R. L. Walquist
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