

Memorandum M-2705

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Auth: DD 254
By: R. R. Everett
Date: 2-15-60

Division 6 - Lincoln Laboratory
Massachusetts Institute of Technology
Cambridge 39, Massachusetts

SUBJECT: Group 62 Section Leaders Meeting -- March 1, 1954

To: Division 6 Group Leaders; Group 62 Staff Members

From: A. P. Kromer

Date: March 2, 1954

Abstract: Tests of IBM mapper with WWI have been completed. An enlarged display section will handle development of display generation and selection frames. IBM will subsequently construct these units. Planning for tests of XD-1 at Poughkeepsie and later at Lexington is being started. Arrangements for installation of XD-1 are being definitized. Typotron demonstration in DID application is to be held March 16.

1. Mapper

The unit developed by IBM has been tested with WWI and returned to IBM. The tests revealed that under certain operating conditions it was possible to have false north markers generated and for the unit to lose synchronism. Necessary corrective action is being investigated.

2. Display System

The engineering work to be done at MIT for the development of the display generation and display selection frames will be done by the display section with Fallows assisting Corderman. The section will be enlarged to include Callahan, Rising, Remis, Gurley, and Shallerer.

A program covering the manpower requirements and a schedule for the work to be done will be prepared.

IBM will procure parts and material and will assemble the display frames per the design developed at MIT.

3. Approval of Components

Components required for circuits in the display system, and not used elsewhere in the system, will be referred to Paine - Watt for approval by the engineer concerned. All drawings and purchase specifications will be prepared at MIT and then forwarded to IBM (Crawford's group) for release and procurement. Preliminary estimates of all known components will be furnished to IBM at an earlier date.

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4. Transistors

Eckl reported that recent contact with G.E. Co. indicates most of their current and future work will be on junction diodes rather than point contact units.

5. Test Planning

Mayer expects to start preparing specs for frames that may be used in connection with planning test conditions for the various units. Jackman and Olsen will work on planning for these tests as previously indicated.

Gus O'Brien is joining Group 62 to do engineering planning for tests to be run after equipment is installed at Lexington. Plans for these tests must be fully coordinated with the unit and frame tests so as to avoid unnecessary duplication and yet completely check functioning of all parts of the system.

6. Power

Amplidynes for marginal checking system have been received by IBM and are being held for later installation.

The MG sets and related switch gear are being installed at Plant 2 for use with XD-2.

DC supplies have had tests started by the manufacturer (G.E. Co.). Delivery of the units for XD-2 has been delayed about two weeks due to shortage of capacitors on order from Sprague.

MG Sets for XD-1 have not been ordered by IBM pending receipt of a quotation from G.E. Co.

7. Installation

Arrangements are being completed to define the areas of responsibility for installation for XD-1, which will be directed by Dodd.

MIT will be responsible for engineering all aspects of design and installation of Power transformers, MG sets, and Power Control & Distribution frame. IBM will do engineering beyond this frame to MCD frames and equipment frames.

Activities of Francis Associates on equipment cooling, lighting and cable fixtures for XD-1 will be directed by MIT. Dodd will be the point of contact for these items.

Ed Quick will maintain contact with the XD-1 installation and will direct installation activity at Plant 2 test areas, and the final XD-2 installation location.

8. Circuits

Dick Best's group will devote some effort to magnetic circuits for possible application in input, output, and display equipment.

9. MTC

The drum is now operating with 17 bits in each of 2 fields. Diode switching has proved very satisfactory to date. Complete information on this has been forwarded to IBM for further study.

The core memory is operating without post write disturb. The cycle time is approximately 6 μ sec and operation is satisfactory.

The MTC will be moved from Whittimore to Lexington between mid-April and June 1.

10. Typotron

Life tests on these tubes is now under way and a demonstration of their use for DID is planned with MTC for March 16. No 5" Charactrons are being ordered pending decision on the use of Typotrons for DID's.

11. Planning

R. Cypser is being reassigned to Poughkeepsie and will work on XD-1 Planning. Dan Ross will work on planning for the Duplex Central for IBM. Steve Dodd will work on this for MIT.

12. Approval and Concurrence

Lincoln approval was issued during this past week for:

- a. Specifications for the Program Element
- b. Specifications for Video mapper
- c. Revision of layout for basement of XD-1 Building

EDO-SO concurrence was reached on the specifications for the Area Discriminator.

13. Meetings for the Week

- Monday - Memory core driver will be discussed at Poughkeepsie by Canty and Shansky
- Tuesday - at Cambridge - Computer controlled tape input system will be discussed

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Meetings for the Week (Continued)

Thursday - at Poughkeepsie - Output frame (black boxes) will be reviewed by EDO-SO for concurrence

Friday - at Poughkeepsie - Power supply spec per IBM Report IM-65 will be reviewed

Signed: _____


A. P. Kromer

APK:hpm

cc: J. W. Forrester
R. R. Everett

Approved: _____


N. H. Taylor

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