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Memorandum M-2797

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Division 6 - Lincoln Laboratory  
Massachusetts Institute of Technology  
Cambridge 39, Massachusetts

CLASSIFICATION CHANGED TO:  
Auth: DD259  
By: P.R. Everett  
Date: 2-15-60

SUBJECT: Group 62 Section Leaders Meeting -- April 26, 1954

To: Division 6 Group Leaders; Group 62 Staff Members

From: P. J. Gray

1. Basic Circuits

Basic circuits group concurrence has been obtained on the following basic circuits:

1. Model B flip flop
2. Model C flip flop
3. Model B Single Shot
4. D. C. Inverter Model A
5. Level Setter Model A
6. Model B Gater tube
7. Model B Level Setter
8. Drum Read Amplifier
9. (Core) Matrix Output Amplifier
10. Power Cathode Followers models F, G, H, J, K, L, & M

MRD write ups are in process on the above circuits.

It is expected that additional circuits will be concurred later this week.

2. Display Consoles

Panels have been drawn up for incorporation in MTC to allow the computer to test prototype display consoles by late summer. Preliminary console block diagrams from Vestal are now being studied.

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### 3. Central Display Frames

After a study of the use flip flops vs. cores for storage of situation display messages it has been found that either method can be used. However, the flip flop method is simpler and involves no more tubes, therefore the use of cores has been dropped. The first attempt at frame layout for the Central Display Frame, covering power distribution etc., is now under study.

A meeting will be held in Cambridge on Friday in an attempt to arrive at answers to the following problems:

- A. The nature of the display consoles to be used for display maintenance and the displays required for that console.
- B. The nature and method of operation of the display console with camera attachment.
- C. Method of distribution of both signals and power to the display consoles (both XD-1 and Duplex).
- D. Method of marginal checking of consoles.
- E. Nature of area discriminator units.

### 4. Logical Drawings

A system has been set up to obtain information quickly on logical diagrams generated at IBM. Cypser of IBM will collect all pertinent diagrams and maintain a file of these. Ron Mayer will have first priority on examining these diagrams and will incorporate information on the overall system diagrams which he is preparing. The IBM drawing will then be returned to Cypser who will maintain a complete file of such drawings.

### 5. Radar Mappers

Revised radar mappers specifications have been reviewed at MIT and have been returned to IBM. There was some feeling that it might be desirable to provide a method for disconnecting the Azimuth protection circuitry so that the mapper could operate on raw Azimuth pulses in the event of failure of this circuitry. However, it is felt that this might better be left as a later modification if it proves necessary.

### 6. Test Planning

A program is under way to provide new engineers for WWI thereby making experienced personnel from WWI available for work on XD-1. Arrangements are being made to provide desk space for installation and test personnel in Building F.

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7. Lincoln Approval and Concurrence

Lincoln Approvals have been forwarded to IBM for the following:

Specifications for Selection Control Frame;  
Second Floor Layout of Building F.

EDO-SO concurrence has been reached on the proposed program of activity on display and XD-I. No Lincoln release letter is contemplated for this item.

Signed:

P. J. Gray  
P. J. Gray

Approved:

A. P. Kromer  
A. P. Kromer

PJG:mo

CC: J. W. Forrester, R. R. Everett

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