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Memorandum M-2760

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Page 1 of 3

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By: R. E. White
Date: 2-15-60

Division 6 - Lincoln Laboratory
Massachusetts Institute of Technology
Cambridge 39, Massachusetts

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SUBJECT: Group 62 Section Leaders Meeting - April 5, 1954
To: Division 6 Group Leaders; Group 62 Staff Members
From: A. P. Kromer
Date: April 6, 1954

Abstract: IBM will furnish monthly status reports relative to time schedules. MTC will move about June 1 and be operative again about July 15. An initial proposal on the duplex central will be distributed about May 1. A separate drum field will be used for Mark X and Fine Grain Data inputs.

1. Status of Construction Activity

Plug-in unit manufacturing for XD-1 and XD-2 is underway and some plug-in unit testing has been started. The DC power supplies and the MG sets are in the process of installation as well as the additional cooling facilities required for Plant 2. Module C of the instruction control frame is expected to be the first module completed and made ready for frame test.

Arrangements have been made by IBM to furnish monthly reports on the status of work as related to their time schedules. This information is for the use of Project Lincoln. R. A. Whitehorne should be notified prior to any discussion of this data with anyone outside of Group 62 so that he may determine whether or not IBM wishes to have a representative present at such discussions.

2. MTC

Latest information regarding the move of MTC to Lexington indicates it will be made about June 1. Based on this, MTC should be back in operation approximately the middle of July. Most of this delay results from need to secure Air Force approvals for certain material and to have the centralized chilled water system put into operation and checked out.

Latest work with the memory on MTC has resulted in measuring the memory cycle at slightly under 5 microseconds.

Some studies using 50% deteriorated tubes in memory circuits indicate the margins to be so wide that it is difficult to locate the poor tube.

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Memorandum M-2760

Page 2 of 3

3. Duplex Center Planning

Periodic meetings of IBM and MIT are continuing in this area. A draft of the initial proposal for the duplex center is to be prepared and distributed about May 1 for comments.

The large amount of switching involved in the duplex mode of operation is being studied to determine whether relay or vacuum tube switching best suits the application.

4. Drums

Agreement was reached to use a separate drum field for input of Mark X automatic height finding and Fine Grain Data (radars). This will be in addition to the two radar input fields.

A meeting will be held in Cambridge on Friday, April 9, to resolve the drum circuit switching question.

5. Outputs

A meeting is to be held at Cambridge Thursday, April 8, to select the type of core device to be used in connection with outputs (shift registers or core memory).

6. Display

M-2732, written by Dick Fallows, covering the proposed procedure for handling the development of the centralized display equipment is being reviewed by IBM and they will forward approval or comments by Thursday, April 8.

7. Power

All major items of power equipment for XD-1 are now on order, with delivery indicated in time to suit the requirements of the XD-1 installation.

8. Logical Design

A block outline of the logic for the XD-1 system has been prepared by Ron Mayer and is available from the Print Room as Sketch SC-47103. Detailed block diagrams for parts of the system will be prepared in the near future. Following this, block schematics for each frame are to be prepared.

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Memorandum M-2760

Page 3 of 3

9. Lincoln Approvals and Concurrence

The following items have been covered by a Lincoln approval letter during the past two weeks:

- a. Radar input counters
- b. Use of 5" Typotron for DID Display
- c. Revision of equipment layout for first floor XD-1 building
- d. Capacity of system to accept Mark X by FGD and automatic height finding data.

The following items have received EDO SO concurrence:

- a. Design specifications for the selection control frame
- b. Proposal for method of handling DID display
- c. Mode of operation for IBM card machines (Types 711, 716, 721)
- d. Layout of equipment for second floor of XD-1 building
- e. Design of false ceiling for first floor of XD-1 building to accommodate interframe wiring.

Signed:

A. P. Kromer
A. P. Kromer

Approved:

N. H. Taylor
N. H. Taylor

APK:hpm

cc: J. W. Forrester
R. R. Everett

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