

Memorandum M-2134

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Digital Computer Laboratory  
Massachusetts Institute of Technology  
Cambridge, Massachusetts

SUBJECT: MTC TESTS ON MAGNETIC MEMORY - LOAD AND CHECK PROGRAM FOR ALL  
ONES OR ZEROES

To: MTC Engineers

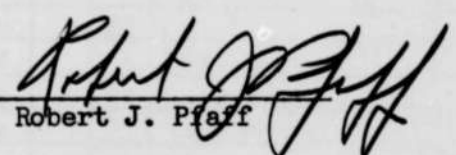
From: R. J. Pfaff

Date: May 5, 1953

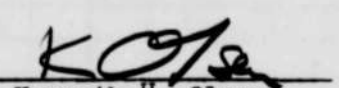
A program designated MP-1 is attached to this Memorandum. Its purpose is to load every register in magnetic memory with the number stored in Register 37. It then proceeds to cycle through the memory, checking each register against Register 37. Once loading is complete no new information is read into the memory. When a failure occurs, a "one" in the Accumulator will designate the digit that failed and the Memory Address Register will contain the address of the faulty register. This check is exclusive of parity check.

It should be noted that putting the same number in each register will result in each plane containing all "one's" or all "zeroes".

Signed

  
Robert J. Pfaff

Approved

  
Kenneth H. Olsen

RJP:jrt

Attached: Memory Test Programs MP-1 and MP-2 SA-54940

