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Memorandum M-2345

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Division 6 - Lincoln Laboratory
Massachusetts Institute of Technology
Cambridge 39, Massachusetts

SUBJECT: IBM PUBLICATIONS
To: Staff Members, Secretaries
From: A. P. Kromer
Date: August 7, 1953

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Below is a list of all IBM publications which have been received at this laboratory and are available for reference in the Whittemore Library. Since demand has been heavy for certain reports, in a few instances there may be a slight delay in obtaining the desired publication. In the future, all IBM publications will be listed as received in a separate section of the Library Accessions List.

Whit. Lib. No.	Title	Author	Cls.
IBM 1	Design Considerations for the Magnetic Matrix Switch	G.E. Whitney	U
IBM 2	Analysis of Flux Reversals in Rectangular Hysteresis Loop Magnetic Cores (Report #1)	G.E. Whitney	U
IBM 3	Analysis of Flux Reversals in Rectangular Hysteresis Loop Magnetic Cores (Report #2)	G.E. Whitney	U
IBM 4	Prototype Equipment	T.A. Burke	S
IBM 5	Summary Report of Magnetic Drum Storage Circuits and Components	J.J. Ingram	C (Ind.)
IBM 6	Survey of Magnetic Drum Storage	J.A. Underhill	C (Ind.)
IBM 7	Magnetic Drum on the Defense Calculator	J.V. Williams, Jr.	C (Ind.)
IBM 8	Report on Drum Read and Write Circuits on the Defense Calculator	(J.E. Helland (N.J. Albanes	C (Ind.)
IBM 9	Magnetic Drum Read/Recording Head Designs	J.V. Williams, Jr.	C (Ind.)
IBM 10	Report on Magnetic Drum Circuits	L.D. Stevens	C (Ind.)
IBM 11	Drum Synchronization	R.L. Harper	U
IBM 12	Summary of Meeting with B.E. Morriss, R.P. Mayer, M. Epstein	B. Housman	C
IBM 13	IBM-MIT Tube-Production-Study Trip	J.A. Goetz	U
IBM 14	Proposed ADC Computer - Descriptions of Instructions (IM-6)	(M.M. Astrahan (B.L. Sarahan (B. Housman (W.H. Thomas	C

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No.	Title	Author	Cls.
IBM 15	Proposed ADC Computer - Organization and Block Diagram (IM-7)	(M.M. Astrahan (B.L. Sarahan (B. Housman (W.H. Thomas	C
IBM 16	Proposed ADC Computer - Reasons for Choice of Instructions (IM-8)	See above	C
IBM 17	Sample Correlation Program		C
IBM 18	Circuit Performance Speculations (IM-9)	(D.J. Crawford (H.D. Ross	U
IBM 19	Proposal on Tube Types to be Used in Circuit Development (IM-10)	(H.D. Ross (D.J. Crawford	U
IBM 20	Proposed Output Display (IM-11)	(E.H. Goldman (W.E. Triest	C
IBM 21	A Magnetic Core Counter Using a Capacitor Storage Transfer Circuit	G.E. Whitney	C (Ind.)
IBM 22	Vols. 1, 2, and 3 of the Systems Diagrams for the IBM Type 701 Machine and assoc. equipment		C (Ind.)
IBM 23	A Two Megacycle Ring Circuit	R.L. West	C (Ind.)
IBM 24	Preliminary Logical Block Diagram of Card Machine Control Circuits for MTC (IM-12)		C (Ind.)
IBM 25	Report of Completion Article I Task 1 (a) Jan. 27, 1953		S
IBM 26	Proposed Input, Output and Auxiliary Memory for WWII (IM-14)	(M.M. Astrahan (B.L. Sarahan (B. Housman (W.H. Thomas	C
IBM 27	Magnetic Drum Head Quality Control and Magnetic Recording Techniques	(H.W. Nordyke (N.J. Albanes	C (Ind.)
IBM 28	D. C. Drum Head Test Results	N.J. Albanes	C (Ind.)
IBM 29	Project High Biweekly Report Feb. 23 to March 6, 1953		U
IBM 30	Whirlwind II Nomenclature Changes (IM-20)	W.H. Thomas	C
IBM 31	A Proposed Method for Storing Radar Returns on a Magnetic Drum (IM-22)	(M.M. Astrahan (B. Housman	C
IBM 32	IBM-MIT Meeting with Sylvania Electric Products Inc.	J.A. Goetz	U
IBM 33	I.R.C. Resistor Meeting, Feb. 19, 1953	J.A. Goetz	U
IBM 34	Life Test of I.R.C. 1% Carbon and Boron-Carbon (BOC) Resistors	J.A. Goetz	U
IBM 35	Trip to Sprague Electric Company	J.A. Goetz	U
IBM 36	Project High Biweekly Report March 8 to March 20, 1953		U
IBM 37	Procedure for Coating a Drum with Magnetic Material	F.E. Hamilton	C (Ind.)
IBM 38	Trip to General Electric Company	N.P. Edwards	U
IBM 39	Trip to RCA, Harrison New Jersey	J.H. Geisler	U

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No.	Title	Author	Cls.
IBM 40	A 100 KC Magnetic Core Shifting Register (IM-23)	(F. Stern-Montagny (G.E. Whitney)	U
IBM 41	Minutes of Joint MIT-IBM Conference	E.H. Goldman	C
IBM 42	Preliminary Operator's Reference Manual (IM-21)		U
IBM 43	List of Commands for Executing Arithmetic Operations		U
IBM 44	Joint Input Output Meeting No. 3	E.H. Goldman	C
IBM 45	Minutes Joint Input Output Meetings 1 - 4	E.H. Goldman	C
IBM 46	Joint Input Output Meeting No. 4	E.H. Goldman	C
IBM 47	Project High Biweekly Report March 23 to April 3, 1953		C
IBM 48	Magnetic Core Procurement and Testing for High Speed Memory (IM-25)	N.P. Edwards	C
IBM 49	Joint Input Output Meeting No. 5	E.H. Goldman	C
IBM 50	Magnetic Core Circuits (IM-5)	G.E. Whitney	U
IBM 51	Project High Biweekly Report April 6 to April 17, 1953		U
IBM 52	Joint Input-Output Meeting No. 6	E.H. Goldman	C
IBM 53	Joint Input-Output Meeting No. 7	E.H. Goldman	C
IBM 54	Project High Biweekly Report April 20 to May 1, 1953		U
IBM 55	Minutes of Basic Circuits Meeting at Hartford	E.H. Goldman	U
IBM 56	Joint Input-Output Meeting No. 9	E.H. Goldman	C
IBM 57	Project High Biweekly Report May 4 to May 15, 1953		U
IBM 58	Joint Input-Output Meeting No. 10	E.H. Goldman	C
IBM 59	Proposed Input-Output System for Transition System Equip. (IM-30)		C
IBM 60	Diode Protection (IM-29)	J. Beesley	U
IBM 61	Project High Biweekly Report May 18 to May 29, 1953		C
IBM 62	A Proposal for Crosstelling	(M.M. Astrahan (R.R. Everett)	C
IBM 63	Project High Biweekly Report June 1 to June 12, 1953		C
IBM 64	Input-Output Operation (IM-34)		U
IBM 65	Report of Completion Article I Task 1 (b) April 27, 1953		S
IBM 66	Short-Video Pulse Generator (IM-32)	E.J. Mulvey	U
IBM 67	Preliminary Report on the Investigation of Control Line Transmission of Data (IM-33)		U
IBM 68	Design Considerations for 64-Position Diode Switches (IM-26)	R.W. Bottomley	U
IBM 69	Electron Tube Application Techniques	R.L. Bullard	U
IBM 70	Input-Output Operation (IM-34)	J.A. Goetz (B. Housman (D. Ross)	U
IBM 71	Change in Store Instruction Timing	B.L. Sarahan	U

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No.	Title	Author	Cls.
IBM 72	Specifications of Test Storage	C. E. Walston	R
IBM 73	Joint Input-Output Meetings No. 11 & 12	E.H. Goldman	C
IBM 74	Project High Biweekly Report June 15 to June 26, 1953		C
IBM 75	Fundamentals of Digital Computer Programming (IM-42)	W.H. Thomas	U
IBM 76	Standard Pulse Width (H-4)	E.H. Goldman	U
IBM 77	Bit Assignment for Instruction Words (H-3)	B.L. Sarahan	U
IBM 78	Capacitor Storage Shifting Register (IM-28)	J.V. Anderson	U
IBM 79	Contact Operated Pulse Generator (IM-40)	J. Hofler	U
IBM 80	Project High Biweekly Report June 29 to July 10, 1953		C
IBM 81	Drum Register Timing	M.M. Astrahan	U
IBM 82	Specifications for Test Storage (IM-37)	C.E. Walston	R
IBM 83	The Instruction Indexing System (IM-35)	G.C. Stierhoff	U
IBM 84	Cathode Follower Design (IM-38)	J.P. Beesley	U
IBM 85	Capacitor Storage Coupling (IM-27)	R. Lindsay	U
IBM 86	Computer Frame and Pluggable Unit Design (IM-36)	B.L. Sarahan	R
IBM 87	Trip Report - Sylvania Electric Products Inc. Headquarters, New York City		U
IBM 88	XD-1 Memory Cycle (H-7)	(H.D. Ross (N.P. Edwards (W.M. Wittenberg (R.P. Mayer	R
IBM 89	Diode Circuit Design (IM-43)	J.P. Beesley	U
IBM 90	Output Display for XD-1 (H-10)	P.W. Rocco	C
IBM 91	Project High Biweekly Report July 13 to July 24, 1953		C
IBM 68-1	Correction List for IM-26	R.L. Bullard	U

Signed: 

Arthur P. Kromer

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CC: R. E. Rader, Div. 2

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