Division 6 - Lincoln Laboratory
Massachusetts Institute of Technology
Cambridge 39, Massachusetts

SUBJECT: ACTIVITIES OF THE BASIC CIRCUIT SUB-COMMITTEE

To: N. H. Taylor, R. A. Nelson, Group 62 Section Leaders and vellum copy to W. Jackman at IBM via Kromer

From: Arthur Heineck

Date: August 3, 1953

1.0 MEMBERS OF THE SUB-COMMITTEE:

MIT	IBM
Art Heineck	Jack Jackman
Bob Callahan	John Foley
	Jim Beesley

2.0 ACTIVITIES OF THE SUB-COMMITTEE:

2.1 Coordination:

The main function of the BCSC is to act as a coordinating unit between the circuit design groups and the users of circuit information. This coordination will be accomplished as follows: from the groups working on the logic of the machine, the Sub-Committee will obtain preliminary specifications for circuits which are needed. These specifications will be distributed to the circuit design groups at both IBM and MIT. Thus, the design groups will know what circuits to work on.

To inform the other groups of the work going on in the circuit design sections, the Sub-Committee will compile a list of all the circuits under development.* This list will give the type of circuit, the circuit engineer, and a reference to one of the entries in the Circuit Application Manual.** Any necessary circuit which is not included

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* See M-2334 A List of Circuits Under Design
** See M-2337 Circuit Application Manual

on the list should be brought to the attention of this Sub-Committee.

2.2 Standardization:

In the final model of the machine, not only will all components be standardized, but also all circuits and methods of interconnecting circuits. The Sub-Committee will aid the Central Standards Committee in standardizing circuits and circuit interconnections as follows:

The BCSC will gather circuit information from the circuit design groups and present this information in summary form in the Circuit Application Manual. The initial entries in this manual will be stamped Tentative. Tentative means that the circuits and circuit information should be used wherever needed, but changes may have to be made in the future. When the summary of any circuit is complete, the Central Standards Committee will distribute the circuit summary for comments and final approval.*

Similarly, information will be gathered on Methods of Interconnecting Circuits and will appear in the Circuit Application Manual. These methods will be approved in a manner much like that used for circuit approval.

Only Approved Circuits and Approved Methods of Interconnecting Circuits will be used in the final model of the machine.

Signed: A. Heineck
A. Heineck

Approved: C. Watt
C. Watt

AH:tl

* See M-2305 Approval of Basic Circuits